

Express Mail No.: EV 130048906 US

9D-RG-19394
PATENT

Reference labels in Figures 1, 2, 3, 4, 7, and 8, and Figures 5 and 6 are fully supported in the specification, and accordingly, Applicant submits that no new matter has been added.

Respectfully Submitted,



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Daum

Serial No.: 09/754,419

Filed: January 5, 2001

For: ARBITRATING CLOCK
SYNCHRONIZATION SYSTEM

Art Unit:

Examiner:

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SUBMISSION OF MARKED UP PARAGRAPHS

Box DAC
Hon. Commissioner for Patents
Washington, D.C. 20231

Submitted herewith are marked up paragraphs in accordance with 37 C.F.R.
1.121(b)(1)(iii), wherein additions are underlined and deletions are [bracketed].

IN THE SPECIFICATION:

At page 1, lines 3-5:

This application claims the benefit of U.S. provisional application No. 60/227,973,
entitled, "Arbitrating Clock Synchronization System", filed on August 25, 2000 [1999].

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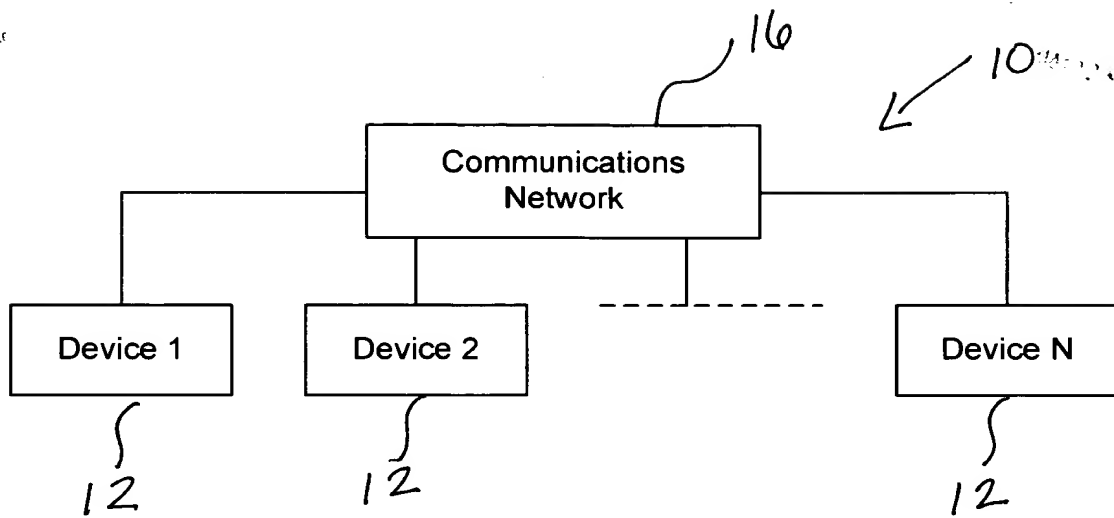


FIG 1

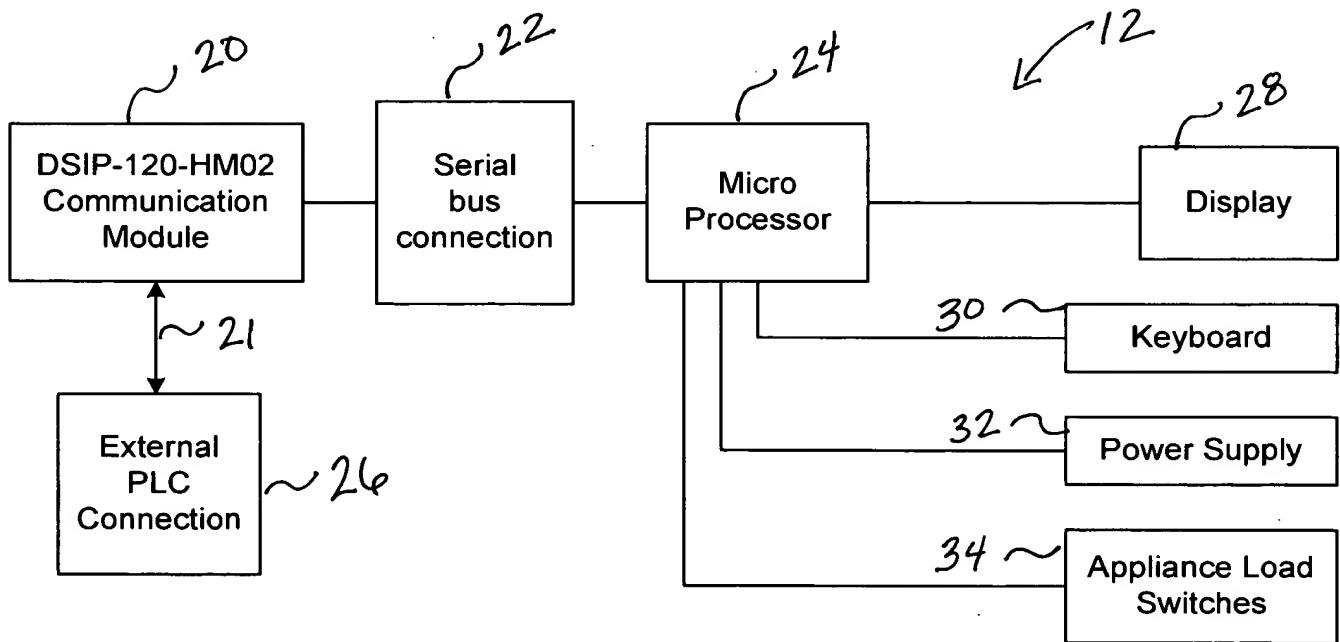


FIG 2

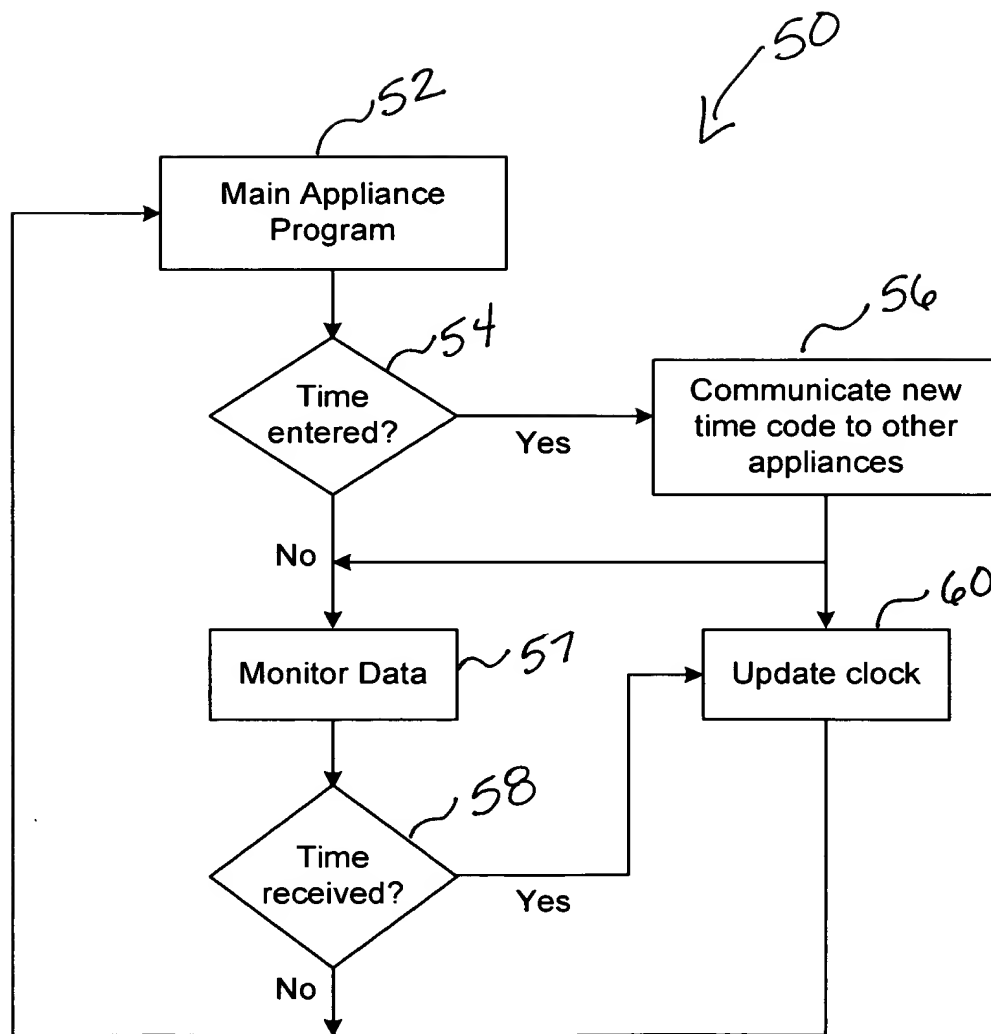


FIG 3

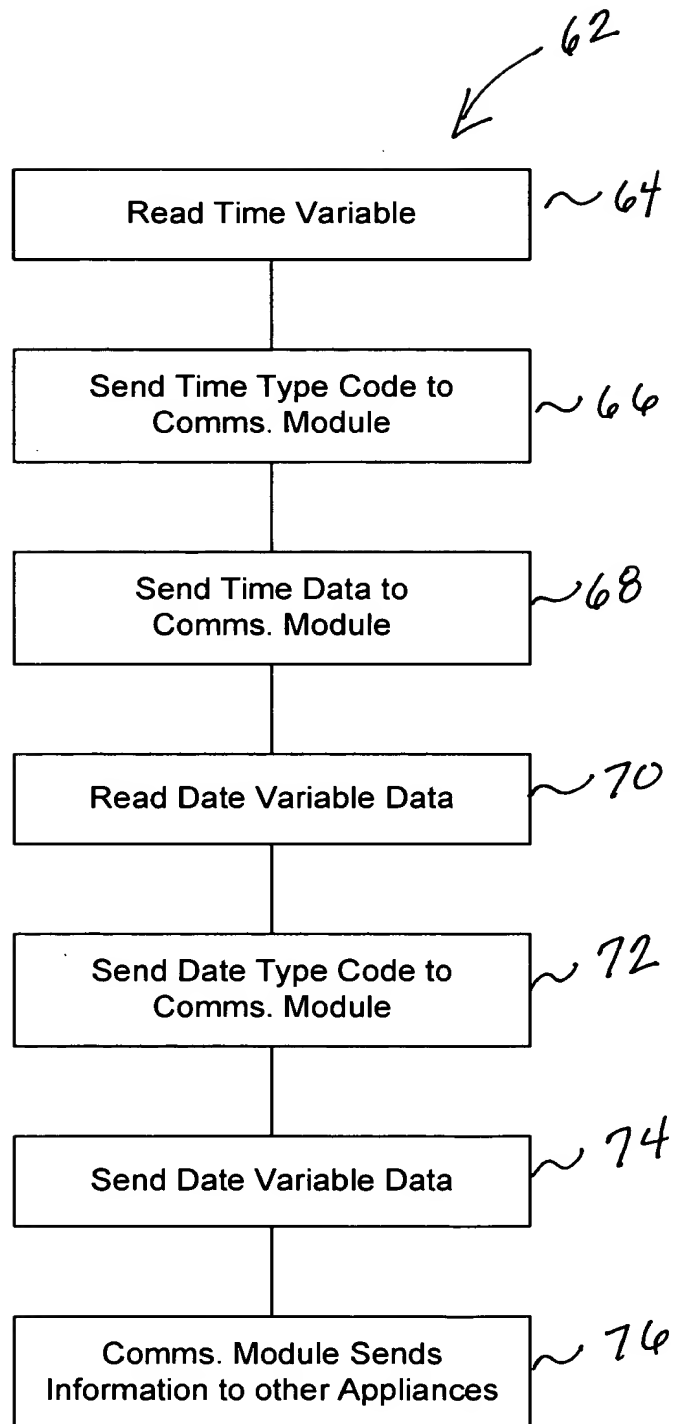


FIG 4

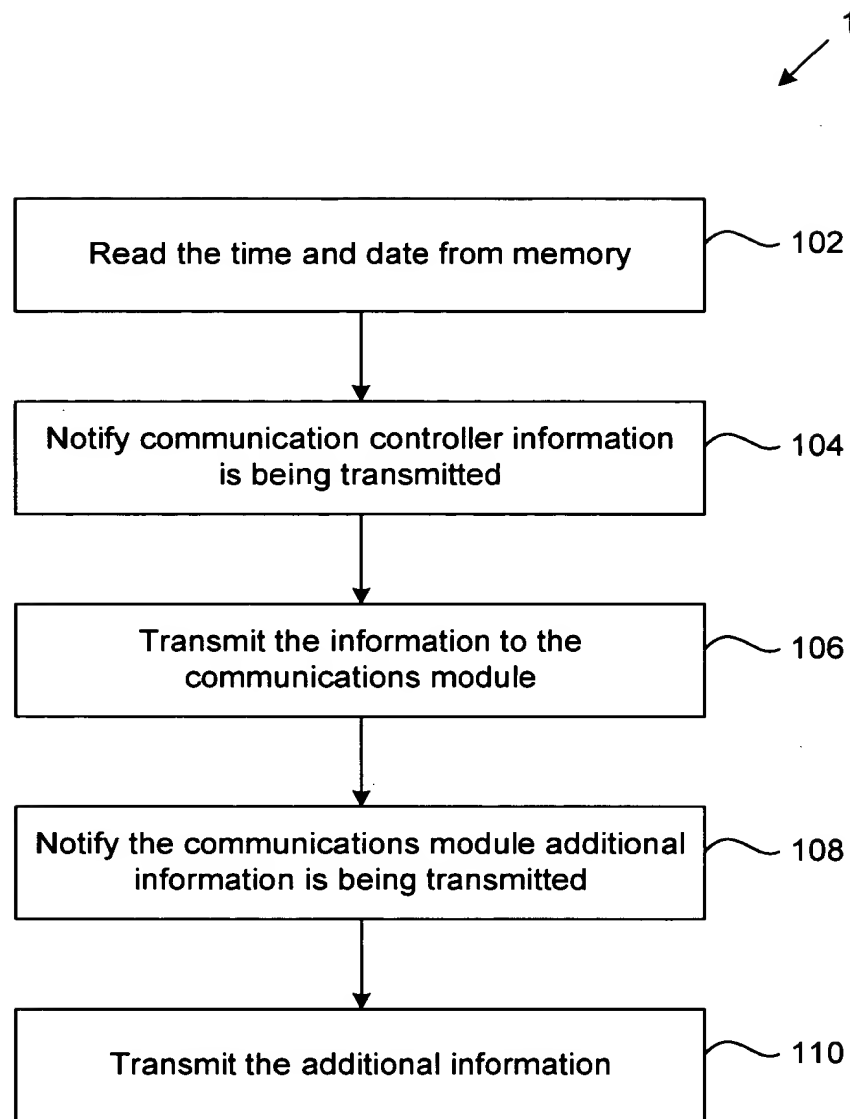


FIG 5

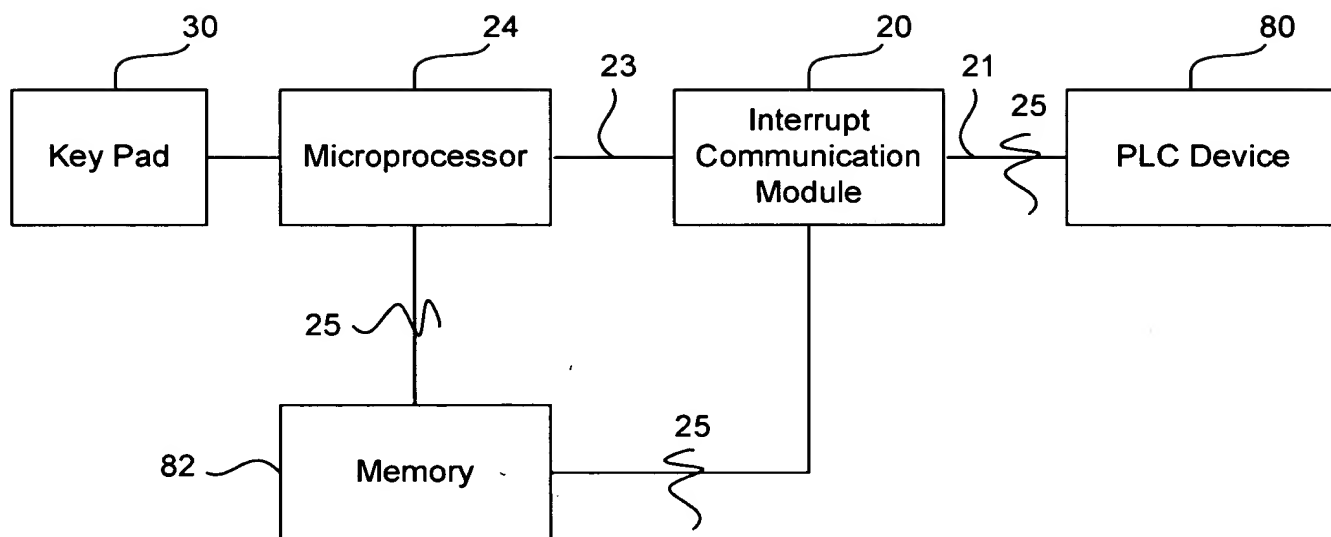


FIG 6

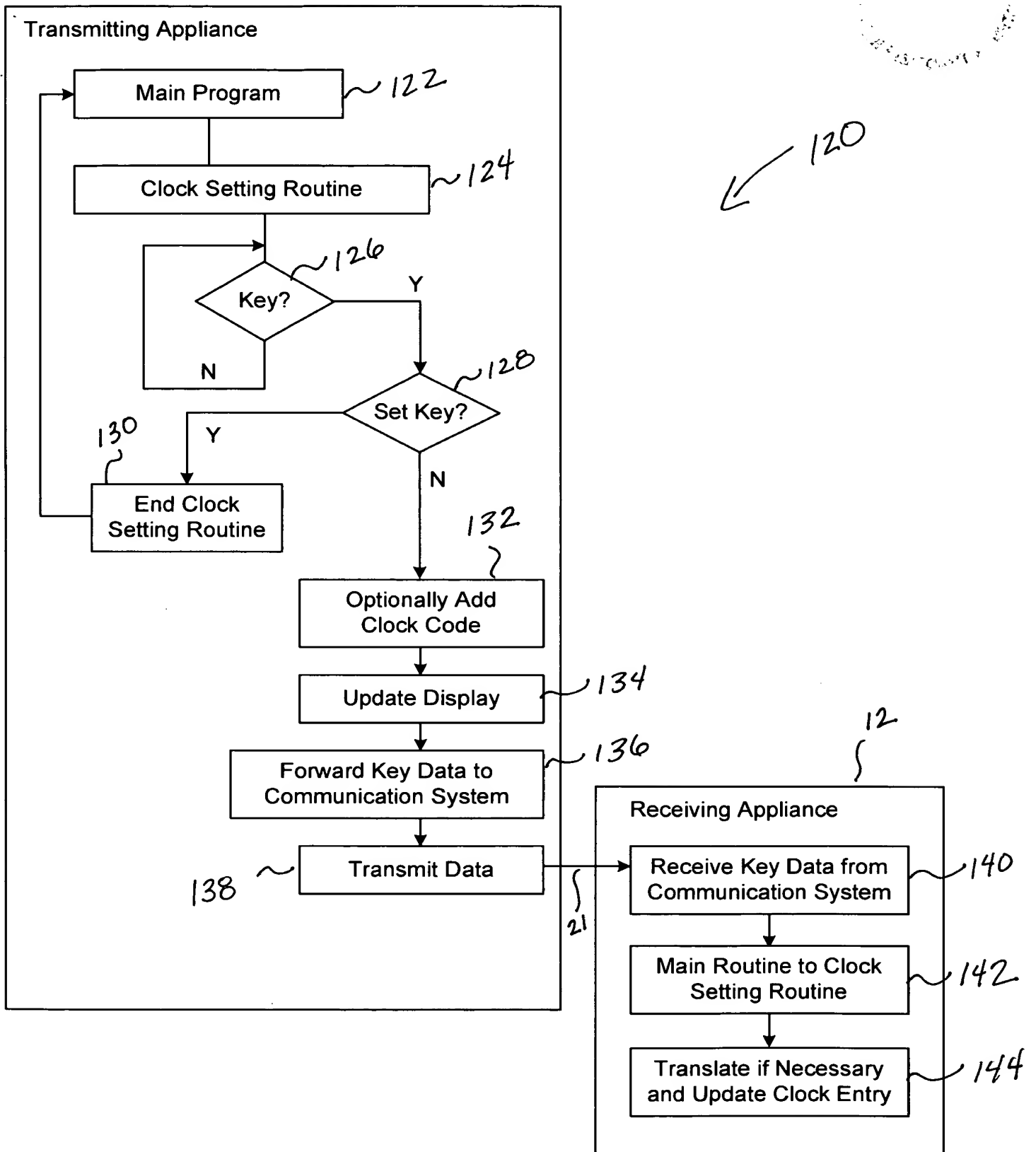


FIG 7

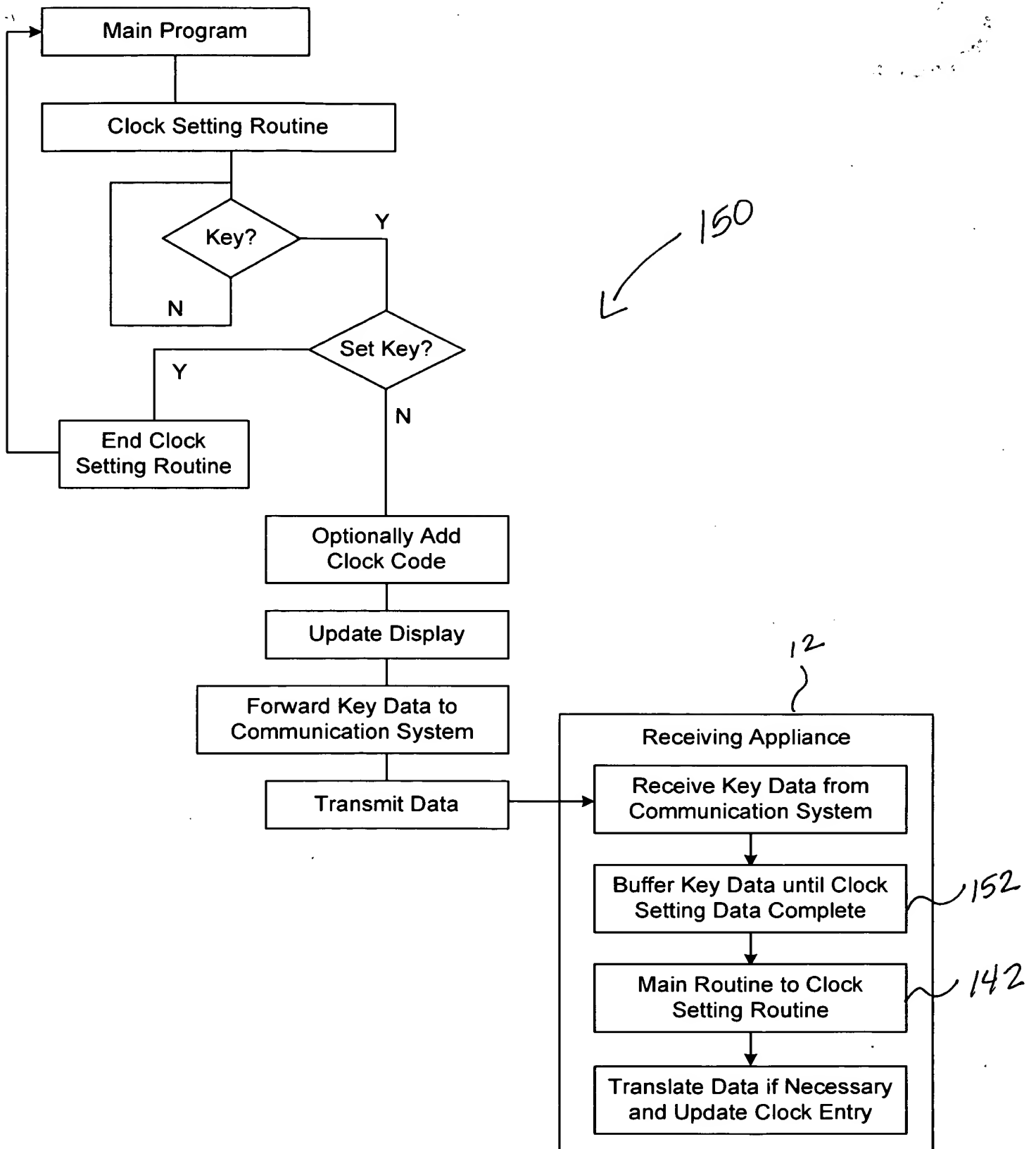


FIG 8